

Applicant Initiated Interview Request Form

Application No.: 10/754,483 First Named Applicant: Kevin Conley
Examiner: Campos, Yaima Art Unit: 2185 Status of Application: Pending

Tentative Participants:

(1) Masako Ando (2) _____
(3) _____ (4) _____

Proposed Date of Interview: TBD Proposed Time: 1:30 PM EDT AM/PM

Type of Interview Requested:

(1) ☒ Telephonic (2) ☐ Personal (3) ☐ Video Conference

Exhibit To Be Shown or Demonstrated: ☒ YES ☐ NO

If yes, provide brief description: Proposed Amendment

Issues To Be Discussed

Issues (Rej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1) <u>§ 103 Rejection</u>	<u>1, 13, 19</u>	<u>Suda & Moro</u>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(2) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(3) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(4) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☐ Continuation Sheet Attached

Brief Description of Argument to be Presented:

The proposed amendment makes clear that the limitation regarding the range of address recites not intended use, but actual store operations.

An interview was conducted on the above-identified application on _____.

NOTE: This form should be completed by applicant and submitted to the examiner in advance of the interview (see MPEP § 713.01).

This application will not be delayed from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of the substance of this interview (37 CFR 1.133(b)) as soon as possible.

/Masako Ando/

Applicant/Applicant's Representative Signature

Masako Ando

Typed/Printed Name of Applicant or Representative

59,900

Registration Number, if applicable

Examiner/SPE Signature

Privacy Act Statement

The **Privacy Act of 1974 (P.L. 93-579)** requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (*i.e.*, GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

PROPOSED AMENDMENT

1. (Currently Amended) A method for reading data from a memory card that provides non-volatile data storage having an address space defined by a contiguous range of addresses, said method comprising:

(a) accessing volume information stored in a range of addresses that is a part of the contiguous range of addresses that defines the address space, the contiguous range of the address space being capable of storing either the volume information or user data depending on a configuration of the memory card;

(b) determining, based on the volume information, whether the non-volatile data storage has a first configuration having a multiple volume address space corresponding to a first file format or a second configuration having a single volume address space corresponding to a second file format;

(c) operating the memory card in accordance with the first file format by dividing the address space of the non-volatile data storage into a plurality of volumes when said determining (b) determines that the memory card has the first configuration, each of the plurality of volumes containing the volume information stored in a respective range of addresses therein; and

(d) operating the memory card in accordance with the second file format by accessing the entire address space of the non-volatile data storage as the single volume when said determining (b) determines that the memory card has the second configuration,

wherein ~~each the~~ range of addresses ~~which stores used to store~~ the volume information in a second and any subsequent volumes under the first configuration ~~stores is used to store~~ user data under the second configuration.

2. (Previously Presented) A method as recited in claim 1,

wherein the memory card includes a switch that has a plurality of switch positions, and

wherein said operating (c) includes at least:

(c1) determining a switch position for the switch; and

(c2) selectively enabling one of the plurality of volumes based on the switch position.

3. (Previously Presented) A method as recited in claim 2,

wherein the switch has at least a first position and a second position, and

wherein said operating (c) further includes at least:

(c3) imposing an address offset when the switch is in the second position,

4. (Original) A method as recited in claim 3, wherein the address offset enables the memory card to provide more data storage capacity than available with a file system using 16-bit addressing.

5. (Previously Presented) A method as recited in claim 2,

wherein the switch has at least a first position and a second position,

wherein, when the switch position is in the first position and the memory card is operated by dividing the address space of the non-volatile data storage into the plurality of volumes, the first volume of the non-volatile data storage is accessed, and

wherein, when the switch position is in the second position and the memory card is operated by dividing the address space of the non-volatile data storage into the plurality of volumes, a second volume of the non-volatile data storage is accessed.

6. (Original) A method as recited in claim 5, wherein the memory card is formatted into either one of a single volume or a pair of volumes, the pair of volumes being the first volume and the second volume.

7. (Original) A method as recited in claim 6, wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the first and second volumes of X/2 gigabytes each as the pair of volumes.

8. (Original) A method as recited in claim 1, wherein said method further comprises:

(e) detecting activation of the memory card, and

wherein said accessing (a), said determining (b), and said operating (c) or (d) are performed once said detecting (e) detects the activation of the memory card.

9. (Original) A method as recited in claim 8, wherein the activation of the memory card occurs upon power-on of the memory card or upon insertion of the memory card into a host device.

10. (Original) A method as recited in claim 1,

wherein the memory card is formatted into a single volume or a plurality of volumes, and

wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the N volumes of X/N gigabytes each as the plurality of volumes.

11. (Previously Presented) A method as recited in claim 1, wherein when said determining (b) determines that the single volume address space is present on the memory card, the first volume has a FAT-32 file format.

12. (Previously Presented) A method as recited in claim 1, wherein when said determining (b) determines that the multiple volume address space is present on the memory card, each of the multiple volumes has a FAT-16 file format.

13. (Currently Amended) A memory card having a single memory array defined by a contiguous range of addresses capable of being configured as multiple partitions each having a first size or as a single partitions having a second size, said memory card comprising:

non-volatile data storage that provides data storage having an address space, said address space being configured to include at least a first partition, the first partition including partition information that is stored in a range of addresses that is a part of the

contiguous range of addresses, the contiguous range of the addresses being capable of storing either the partition information or user data depending on a configuration of the memory card;

a switch being set in one of a plurality of switch positions; and

a controller that manages access to the data stored in said non-volatile data storage,

wherein said controller ~~examines is configured to examine~~ the partition information stored in said first partition to determine whether the memory card has a first configuration using the single partition in accordance with a first file format or a second configuration using the multiple partitions in accordance with a second file format based on the partition information,

wherein when said controller determines that the single partition is used, the entire address space of said non-volatile data storage is accessed as a single partition, the first partition being the single partition, and

wherein when said controller determines that the memory card has the first configuration, the address space of said non-volatile data storage is divided into multiple partitions, each of the plurality of partitions containing the partition information stored in a respective range of addresses therein, one of the multiple partitions being accessed based on the switch position of said switch,

wherein when said controller determines that the memory card has the second configuration using the single partition, the entire address space of said non-volatile data storage is accessed as a single partition, the first partition being the single partition,

and wherein ~~each the~~ range of addresses ~~which stores used to store~~ the partition information in a second and any subsequent partitions under the first configuration ~~stores is used to store~~ user data under the second configuration.

14. (Original) A memory card as recited in claim 13, wherein the one or more partitions are volumes.

15. (Original) A memory card as recited in claim 13, wherein when said controller determines that the single partition is being used on said memory card, the single partition has a FAT-32 file format.

16. (Original) A memory card as recited in claim 13, wherein when said controller determines that the multiple partitions are being used, each of the multiple partitions has a FAT-16 file format.

17. (Original) A memory card as recited in claim 13,

wherein said memory card is formatted into either a single partition or a plurality of partitions, and

wherein the total non-volatile data storage for said memory card is formatted to a first partition of X gigabytes as the single partition, or formatted into the N partitions of X/N gigabytes each as the plurality of partitions.

18. (Original) A memory card as recited in claim 13, wherein said memory card is a FLASH memory device.

19. (Currently Amended) A memory device that provides non-volatile data storage having an address space defined by a contiguous range of addresses, said memory device comprising:

means for accessing volume information stored in a range of addresses that is a part of the contiguous range of addresses that defines the address space, the contiguous range of the address space being capable of storing either the volume information or user data depending on a configuration of the address space;

means for determining whether the memory device has a first configuration using a multiple volume address space corresponding to a first file format or a second configuration using a single volume address space corresponding a second file format based on the volume information; and

means for operating said memory device based on the configuration of said memory device, including:

means for dividing the address space of the non-volatile data storage into multiple volumes if said means for determining determines that the memory device has the first configuration, each of the multiple volumes containing the volume information stored in a respective range of addresses therein; and

means for accessing the entire address space of the non-volatile data storage as a single volume if said means for determining determines that the memory device has the second configuration, wherein ~~each the~~ range of addresses which stores ~~used to store~~ the volume information in a second and any subsequent volumes under the first configuration stores ~~is used to store~~ user data under the second configuration.

20. (Previously Presented) A memory device as recited in claim 19,

wherein said means for operating operates said memory device in a first mode when said means for determining determines that the multiple volume address space is present on said memory device, and

wherein said means for operating operates said memory device in a second mode when said means for determining determines that the single volume address space is present on said memory device.

21. (Original) A memory device as recited in claim 20, wherein said memory device further comprises a switch means that permits user selection of one of the first mode and the second mode.

22. (Previously Presented) A memory device as recited in claim 19, wherein said means for operating operates said memory device as providing only the first volume when said means for determining determines that the single volume address space is present on said memory device.

23. (Previously Presented) A memory device as recited in claim 19, wherein said means for operating operates said memory device as providing a plurality of volumes when said means for determining determines that the multiple volume address space is present on said memory device, one of the plurality of volumes being the first volume.

24. (Original) A memory device as recited in claim 19, wherein said memory device is a memory card.

25. (Original) A memory device as recited in claim 19, wherein said memory device is a FLASH memory device.

26 – 35. (Canceled).